



**UNIVERSITAS NEGERI YOGYAKARTA**  
**FACULTY OF MATHEMATICS AND NATURAL SCIENCES**  
**DEPARTMENT OF SCIENCE EDUCATION**  
Jalan Colombo Nomor 1 Yogyakarta 55281  
Telepon (0274) 565411 Pesawat 217, (0274) 565411 (TU), fax (0274) 548203  
Laman : fmipa.uny.ac.id, E-mail : humas\_fmipa@uny.ac.id

**Bachelor of Education in Science**

**MODULE HANDBOOK**

Module name:	<b>Introduction to Electronics Skills</b>
Module level, if applicable:	Undergraduate
Code:	IPA6237
Sub-heading, if applicable:	-
Classes, if applicable:	-
Semester:	7 <sup>th</sup>
Module coordinator:	Sumarna, M.Si.
Lecturer(s):	Widodo Setyowibowo, M.Si.
Language:	Bahasa Indonesia
Classification within the curriculum:	Compulsory Course
Teaching format / class hours per week during the semester:	100 minutes lectures and 120 minutes structured activities per week.
Workload:	Total workload is 90,67 hours per semester which consists of 100 minutes lectures, 120 minutes structured activities, and 120 minutes individual study per week for 16 weeks.
Credit points:	2 SKS (3 ETCS)
Prerequisites course(s):	-
Targeted learning outcomes:	<p>This course provides competencies for students to understand basic principles and have the knowledge and skills to:</p> <ul style="list-style-type: none"><li>CO1. analyze and design functional electronic circuits that include electronic</li><li>CO2. measuring devices (volt-meters, ohm-meters, amperemeters, and oscilloscopes / CROs)</li><li>CO3. DC circuits (series-parallel concepts, voltage dividers, current dividers, Kirchoff's law, current sources)</li><li>CO4. AC circuits (inductor-transformers, capacitors, effective values, RLC circuits, generator)</li><li>CO5. discrete semiconductor circuits (rectifier, filter, voltage regulator, transistor amplifier),</li></ul>

	CO6. analog integrated circuit (operational amplifier, timer 555, oscillator), digital integrated circuit (logic gate, flip-flop, counter, and register)																							
Content:	This course provides competencies for students to understand basic principles and have the knowledge and skills to analyze and design functional electronic circuits that include electronic measuring devices (volt-meters, ohm-meters, amperemeters, and oscilloscopes / CROs) , types and functions of electronic components (active components, passive components), DC circuits (series-parallel concepts, voltage dividers, current dividers, Kirchoff's law, current sources), AC circuits (inductor-transformers, capacitors, effective values, RLC circuits, generator), discrete semiconductor circuits (rectifier, filter, voltage regulator, transistor amplifier), analog integrated circuit (operational amplifier, timer 555, oscillator), digital integrated circuit (logic gate, flip-flop, counter, and register), and pre -work (mic pre-amp, LDR pre-amp, electronic thermometer, simple audio amplifier, multivibrator, pulse counter, etc.).																							
Study / exam achievements:	<p>Attitude assessment is carried out at each meeting by observation and/or self-assessment techniques using the assumption that basically every student has a good attitude. The student is given a value of very good or not good attitude if they show it significantly compared to other students in general. The result of attitude assessment is not a component of the final grades, but as one of the requirements to pass the course. Students will pass from this course if at least have a good attitude.</p> <p>The final mark will be weight as follow:</p> <table><tr><th>No</th><th>CO</th><th>Assessment Object</th><th>Assessment Technique</th><th>Weight</th></tr><tr><td rowspan="5">1</td><td rowspan="5">CO1, CO2, CO3, CO4,</td><td>a. Individual Assignment</td><td rowspan="5">Presentation / written test</td><td>15%</td></tr><tr><td>b. Group Assignment</td><td>15%</td></tr><tr><td>c. Quiz</td><td>25%</td></tr><tr><td>d. Mid</td><td>30%</td></tr><tr><td>e. Final Exam</td><td></td></tr><tr><td colspan="4">Total</td><td>100%</td></tr></table>	No	CO	Assessment Object	Assessment Technique	Weight	1	CO1, CO2, CO3, CO4,	a. Individual Assignment	Presentation / written test	15%	b. Group Assignment	15%	c. Quiz	25%	d. Mid	30%	e. Final Exam		Total				100%
No	CO	Assessment Object	Assessment Technique	Weight																				
1	CO1, CO2, CO3, CO4,	a. Individual Assignment	Presentation / written test	15%																				
		b. Group Assignment		15%																				
		c. Quiz		25%																				
		d. Mid		30%																				
		e. Final Exam																						
Total				100%																				
Forms of media:	Board, LCD Projector, Laptop/Computer																							
Literature:	<ol style="list-style-type: none"><li>1. Smith, Ralph J. 1995. Circuits, Devices, and Systems. John Wiley &amp; Sons.</li><li>2. Schuler. 1989. Electronics Principles and Applications. McGrawHill</li></ol>																							

## PLO and CO mapping

	PLO											
	Attitude			Knowledge				Specific Skill				
	PLO1	PLO2	PLO3	PLO1	PLO2	PLO3	PLO4	PLO1	PLO2	PLO3	PLO4	PLO5
CO1				✓	✓	✓						
CO2					✓		✓					
CO3				✓			✓					
CO4					✓	✓		✓				
CO5						✓	✓					
CO6				✓					✓			